160 External Memory 100 140 150 Bus Bridge Master3 Slave3 114 Decoder 132 Master2 Slave2 Arbiter Host Processor (CPU) Master1 Slave1

ĩ.

Fig. 2

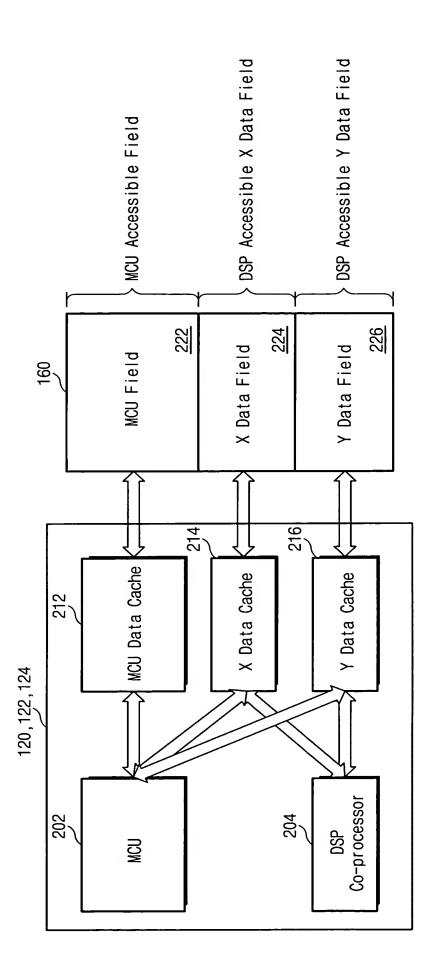


Fig. 3

